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Application Number	09/883,139
Filing Date	June-15, 2001
First Named Inventor	James Lynn
Group Art Unit	2182
Examiner Name	Alan S. Chen
Attorney Docket Number	LSI01023 / 01-023

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I am the:

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- ☒ Assignee of Record of the entire interest  
*Certificate under 37 CFR 3.73(b) is enclosed*

**SIGNATURE of Applicant or Assignee of Record**

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
Date	8-21-04



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PTO/SB/122 (11/96)

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## CHANGE OF CORRESPONDENCE ADDRESS Application

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Application Number	09/883,139
Filing Date	June-15, 2001
First Named Inventor	James Lynn
Group Art Unit	2182
Examiner Name	Alan S. Chen
Attorney Docket Number	LSI01023 / 01-023

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## SIGNATURE of Applicant or Assignee of Record

Typed or Printed Name	SANDEEP JAGGI, Chief IP Counsel, Assistant Corporate Secretary, LSI Logic
Signature	
Date	8-31-04

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re Application of:**

James Lynn et. al.

**Serial No.:**

09/883,139

**Group Art Un**

2182

**Filed:**

June 15, 2001

**Examiner:**

Chen, Alan S.

**Atty Docket:**

LSI01023 / 01-023



Method to Support General  
Enclosure Wiring When  
Associating SES Data with Physical  
Device on a Fiber Channel Loop  
with Soft Addresses

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**CERTIFICATION UNDER 37 C.F.R. 3.73(b)**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
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Date:

8/31/07

Respectfully submitted,

*Sandeep Jaggi*  
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Chief IP Counsel  
Corporate Assistant Secretary  
LSI LOGIC CORPORATION